Effect Of Value Of Resistance Announcement To Thd In Electrical System Faculty Of Engineering University Udayana Denpasar

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Abstract AC (Air Conditioner), TV, lamp, LCD and others are categorized as nonlinear load generating harmonics. The high content of harmonics has a negative impact on the quality of electrical power. The result of measurement of THD (Total Harmonic Distortion) current at 9 ohm ground resistance value at phase R = 9,8%, phase S = 11,0%, phase T = 7,5% exceed from standard value IEEE 519 Year 2014 equal to ≤ 5 , 0%, THD measurement of phase voltage R = 1.4%, phase S = 1.2%, phase T = 1.3%, meet the maximum THDv standard of ≤ 5%. To overcome this problem, it was observed the effect of the value of ground resistance to THDi and THDv on the electrical system of the College of Engineering Faculty of Denpasar. This method of analysis with ETAP software simulation.

With grounding system ≤ 3 ohm only effect on current THD only with decreasing value of THDi become: phasa R = 4,2%, phase S = 5% and phase T = 4,3%, meaning with grounding system ≤ 3 ohm THD current fulfill IEEE 519 - 2014 standard <5%. While THD voltage has no effect (THD value of fixed voltage). Several studies have suggested that THD repair can be done by filter installation. However in this study the installation of grounding system ≤ 3 ohms can reduce the value of current THD.

Index Terms—Grounding System, Harmonics, Power Quality

I. INTRODUCTION

Lights, Air Conditioner, TV, LCD and others are non linear load type, current wave and output voltage are not proportional to the waveforms, causing distortion in current and voltage waves known as harmonics. The harmonics content in the customer has a negative impact on the quality of electric power and can damage the equipment due to the rise in temperature on the cable and electrical panel connectors.

The result of THDi measurement at peak load in electrical system of Faculty of Engineering Denpasar at 9 ohm ground resistance, phase R = 9.8%, phase S = 11.0%, phase T = 7.5%, exceeds standard value IEEE 519 Year 2014 ≤ 5.0%, THD (Total Harmonic Distortion) voltage of phase R = 1.4%, phase S = 1.2%, phase T = 1.3%, the condition meets the maximum THDv standard of ≤ 5%. will continuously lead to decreased power quality and result in damage to equipment on the electrical panel.

Based on the problem is done research of the influence of the value of grounding resistance to THD current and THD voltage on the electrical system. to maintain the quality of electrical power.

II. REVIEW OF LITERATURES

2.1 State of The Art

1. Non-linear loads contribute greatly to the increase in THD, the result of analysis of THDi percentage of 34.80% and 25.67%, when non-linear loads are fully operational [1].
2. Soil at Faculty of Engineering Udayana University Denpasar measured soil resistance value = 30.14 ohm-m, depth of electrode installation at least 13 meters to get ground resistance value < 3 Ohm [2].

3. The research of Janardana (2016) in Faculty of Engineering of Udayana University Denpasar obtained the characteristics of each grounding system such as 1 rod, 2 rod s < L, 2 rod s > L, plates and grids to obtain the ground resistance < 3 ohm [3].

2.2 Grounding System
The grounding system is installed to drain the lightning current to the ground, so that the system and humans around it avoid the lightning strikes. The four parts of the electrical installation that must be earthed are [4], [5], [6]:

a) The installation parts are made of metal and can easily be touched humans.
b) Electric discharge section lightning arrester.
c) Lightning wire at the top of the transmission line.
d) The neutral point of the transformer or the neutral point of the generator.

2.3 Harmonization
Harmonics arise due to the operation of non-linear electrical loads. The high harmonics result in poor power quality [7], [8], [9].

Two harmonic criteria are current harmonic distortion (THDi) and harmonic strain (THDv).

| TABLE I | HARMONIC CURRENT DISTORTION LIMIT WITH A VOLTAGE RATING OF 120V TO 69KV [10] |
|-----------------------------------------------|
| Maximum harmonic current distortion in percent of I_L | Individual harmonic ordee (odd harmonic)^a,b |
| I_{sc}/I_L | 3≤h≤11 | 11≤h≤17 | 17≤h≤23 | 23≤h≤35 | 35≤h≤50 | THD |
| < 20 | 4.0 | 2.0 | 1.5 | 0.6 | 0.3 | 5.0 |
| 20 < 50 | 7.0 | 3.5 | 2.5 | 1.0 | 0.5 | 8.0 |
| 50 < 100 | 10.0 | 4.5 | 4.0 | 1.5 | 0.7 | 12.0 |
| 100 < 1000 | 12.0 | 5.5 | 5.0 | 2.0 | 1.0 | 15.0 |
| >1000 | 15.0 | 7.0 | 6.0 | 2.5 | 1.4 | 20.0 |

THDi harmonics even sequence is limited to 25% of the odd-order harmonics above.

Maximum harmonic current distortion in percent of I_L
The current distortion caused by a dc wave rectifier dc is not permitted or not included in the table above.

With:

I_{sc} = Max short circuit current di PCC (Point of Common Coupling)
I_L = Max load current (fundamental load flow) di PCC

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I_{sc} = Max short circuit current in PCC (Point of Common Coupling).
I_L = Max load current (fundamental load flow) in PCC.

According to IEEE Standard 519-2014, to know the maximum THD limit on the utility, we must know the short-circuit ratio. SC_ratio is searched by the formula:

SC_ratio = \frac{I_{sc}}{I_L} = \frac{I_{sc}}{I_L} \quad (1)

Where, I_{sc} (short circuit current) is searched by the formula:

I_{sc} = \frac{KVA \times 100}{\sqrt{3} \times KV \times Z(\%) \times PCC} \quad (2)

IL (maximum load current) is searched by the formula:

I_L = \frac{KW}{PF \times \sqrt{3} \times KV} \quad (3)

I_{sc} = Maximum short circuit current on PCC.
I_L = Maximum load current.
KW = Total power active

| TABLE II | VOLTAGE DISTORTION LIMIT [10] |
|-----------------------------------------------|
| BusVoltage at PCC | Individual Voltage Distortion (%) | Total harmonic distortion THD (%) |
| V ≤ 1.0 kV | 5.0 | 8.0 |
| 1 kV < V < 69 kV | 3.0 | 5.0 |
| 69 kV < V ≤ 161 kV | 1.5 | 2.5 |
| 161 kV < V | 1.0 | 1.5 |

2.4 Non-Linear Load
Nonlinear loads result in harmonics that have impedances that are not constant every period of inputs [11].
III. RESEARCH METHODS

3.1 Research Sites

The research was conducted at Faculty of Engineering Udayana University of Denpasar and Energy Laboratory of Electrical Engineering.

3.2 Research Steps

1. Data collection of non-linear load amounts.
2. Calculate the total capacity of active power (watt) non-linear load.
3. THD current analysis and THD voltage by doing simulation using ETAP software.
4. THDi maximum limit based on IEEE standard 519-2014.

3.3 Data Analysis

1. THD current analysis and THD voltage by doing simulation using ETAP software.
2. THDi maximum limit based on IEEE standard 519-2014.

IV. RESULTS AND DISCUSSION

### TABLE III
MEASUREMENTS OF THDI AND THDV ON 9 OHM GROUND RESISTIVITY VALUES

<table>
<thead>
<tr>
<th>No.</th>
<th>Measurements</th>
<th>THD Current Value (%)</th>
<th>THD Voltage Value (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>I, 4 – 9 - 2017</td>
<td>R = 9.8, S = 11.0, T = 7.5</td>
<td>R = 1.4, S = 1.2, T = 1.3</td>
</tr>
<tr>
<td>2</td>
<td>II, 5 – 9 - 2017</td>
<td>R = 9.9, S = 11.0, T = 7.5</td>
<td>R = 1.4, S = 1.2, T = 1.3</td>
</tr>
<tr>
<td>3</td>
<td>III, 5 – 9 - 2017</td>
<td>R = 9.8, S = 11.0, T = 7.5</td>
<td>R = 1.4, S = 1.2, T = 1.3</td>
</tr>
</tbody>
</table>

4.1 Analysis of Harmonic Standard Determination

THDi (Current) and THDv (Voltage)

### TABLE IV
DATA ON CURRENT MEASUREMENT RESULTS AND VOLTAGE AT MAXIMUM EXPENSE

<table>
<thead>
<tr>
<th>No</th>
<th>Fa</th>
<th>Sa</th>
<th>V Volt</th>
<th>Cos φ</th>
<th>P Watt</th>
<th>S VA</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>R</td>
<td>64</td>
<td>221</td>
<td>0.8</td>
<td>1212</td>
<td>1426</td>
</tr>
</tbody>
</table>

Data Trafo:

Transformer Capacity (kVA) = 160 kVA
Secondary Voltage (kV) = 380 V = 0.38 kV
Impedansi (%) = 4%

Analysis $Sc_{ratio}$:

$$Sc_{ratio} = \frac{I_{sc}}{I_L}$$

$$I_{sc} = \frac{KVA \times 100}{\sqrt{3} \times K V \times Z \%(\%)}$$

KVA trafo = 160, KV trafo = 0.38
Impedansi Z = 4% = 0.04, then:

$$I_{sc} = \frac{160 \times 100}{\sqrt{3} \times 0.4 \times 0.04} = \frac{16000}{\sqrt{3} \times 0.016}$$

$$I_L = \frac{KW}{PF \times \sqrt{3} \times K V} = \frac{20.113}{0.85 \times \sqrt{3} \times 0.4}$$

$$= \frac{35993.844 A}{0.4}$$

After the $I_{sc}$ and IL values are obtained, the current THD standard can be determined:

$Sc$ ratio = $I_{sc}$ / $I_L$ = 16.90449, as in table 5 below, $Sc$ value <20, then the standard THDi (current) is maximum 5% as table 1.

The result of the measurement of 9 ohm earthing system, the peak load THD current > 5%, according to the results of analysis and IEEE 519-2014 standards should be <5%, so the quality of power system power is still low, it is one of the causes of the frequent parent panel (MDP) is damaged.

The treatment was performed by paralleling the grounding system of ≤ 3 ohms resistance to MDP, measured at peak load, as shown in Table 5.
TABLE V
RESULTS MEASUREMENTS THDI AND THDV
GROUNDING SYSTEM R ≤ 3 OHM

<table>
<thead>
<tr>
<th>No</th>
<th>Measurements</th>
<th>THD Current Value (%)</th>
<th>THD Voltage Value (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>I, 11 – 9 - 2017</td>
<td>R = 4.2</td>
<td>R = 1.4</td>
</tr>
<tr>
<td></td>
<td></td>
<td>S = 5.0</td>
<td>S = 1.2</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T = 4.3</td>
<td>T = 1.3</td>
</tr>
<tr>
<td>2</td>
<td></td>
<td>R = 4.2</td>
<td>R = 1.4</td>
</tr>
<tr>
<td></td>
<td></td>
<td>S = 5.0</td>
<td>S = 1.2</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T = 4.3</td>
<td>T = 1.3</td>
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<tr>
<td>3</td>
<td>II, 12 – 9 - 2017</td>
<td>R = 4.2</td>
<td>R = 1.4</td>
</tr>
<tr>
<td></td>
<td></td>
<td>S = 5.0</td>
<td>S = 1.2</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T = 4.3</td>
<td>T = 1.3</td>
</tr>
</tbody>
</table>

4.2 Discussion
With grounding system ≤ 3 ohm only effect on THD current with decrease of THD value respectively become: phase R = 4.2%, phase S = 5%, phase T = 4.3%, means grounding system ≤ 3 ohms THD current decreased according to IEEE 519 - 2014 < 5% standards. whereas THD voltage has no effect (THDv value is fixed). Several studies have suggested that THD repair can be done by filter installation. However, in this study the installation of ≤ 3 ohm grounding system can reduce the value of THD.

V. CONCLUSION
Based on the results of the analysis concluded, among others:
1. The result of sc value analysis < 20, then the standard of THDi (current) according to IEEE 519 - 2014 standard) is maximum 5%.
2. The grounding system with a resistance value of ≤ 3 ohm contributes to lowering the current THD value fairly well and delivering the value according to IEEE 519 - 2014 standard is <5% for current THD and THD voltage <5%.
3. Based on the measurement of the parent panel with the grounding system ≤ 3 ohm only affects the THD current with the decrease of THD value of each to: phase R = 4.2%, phase S = 5% and phase T = 4.3% means by grounding system ≤ 3 ohm THD current becomes smaller than IEEE 519 - 2014 standard that is <5%, whereas THD voltage has no effect (has a fixed voltage THD value). In some studies mentioned THD repair can be done with the installation of filters.

However, in this study, installation of grounding systems that meet the standards can reduce the value of THD.

REFERENCE